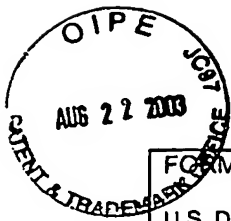


<b>FORM PTO-1449 (SUBSTITUTE)</b>  U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE  <b>INFORMATION DISCLOSURE          STATEMENT BY APPLICANT</b> (37 CFR 1.98(b))				Attorney Docket No.: MUH-12728 Appl. No.: <u>10/631,350</u>  Applicant: HANS WEBER ET AL.  Filing Date: July 31, 2003 Group Art Unit: <u>2818</u>			
EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
<i>dh</i>	A	5,736,445	4/7/98	Pfirsch			
	B						
	C						
	D						
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<b>FOREIGN PATENT DOCUMENT</b>							
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES   NO
No Copies provided <i>dh</i>	J	195 36 183 C1	9/12/96	Germany			
	K	199 53 620 A1	5/11/00	Germany			
	L	197 50 221 A1	5/20/98	Germany			
	M	2 319 395 A	5/20/98	U.K.			
	N						
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)</b>							
EXAMINER <i>Neas</i>				DATE CONSIDERED <u>08/01/05</u>			



FORM PTO-1449 (SUBSTITUTE) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE  INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))	Attorney Docket No.: MUH-12728  Applicant Hans Weber et al.  Filing Date July 31, 2003  Group Art Unit 2818
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## U.S. PATENT DOCUMENTS

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
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## FOREIGN PATENT DOCUMENT

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES   NO
	J						
	K						
	L						
	M						
	N						

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

No copy provided	O	Bobby J. Daniel et al.: "Modeling of the CoolMOS™ Transistor—Part I: Device Physics", <i>IEEE Transactions on Electron Devices</i> , Vol. 49, No. 5, May 2002, pp. 916-920
	P	

EXAMINER 	DATE CONSIDERED 08/05/05
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.